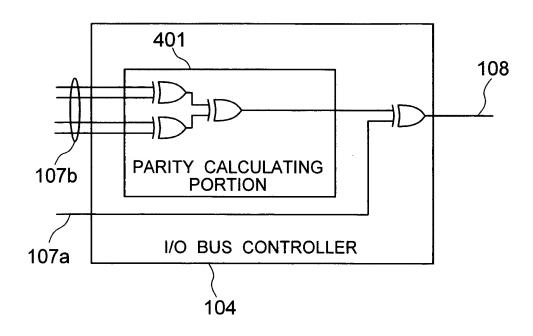


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107 107a FAULT GENERATION REGISTER FAULT GENERATION STATE REGISTER FAULT GENERATOR 303 PARITY GENERATING PORTION 130/ DEVICE CONTROLLER 126 302 I/O BUS INTERFACE CIRCUIT 301

F1G. 3

## FIG. 4



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FIG. 5

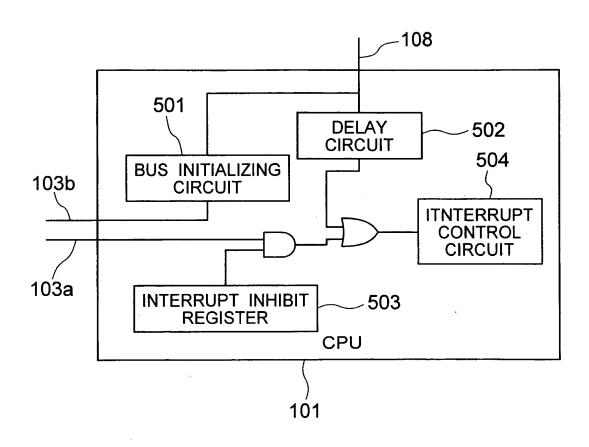


FIG. 6

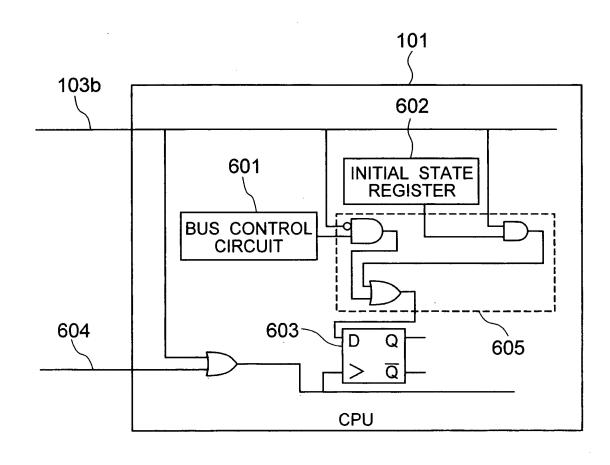
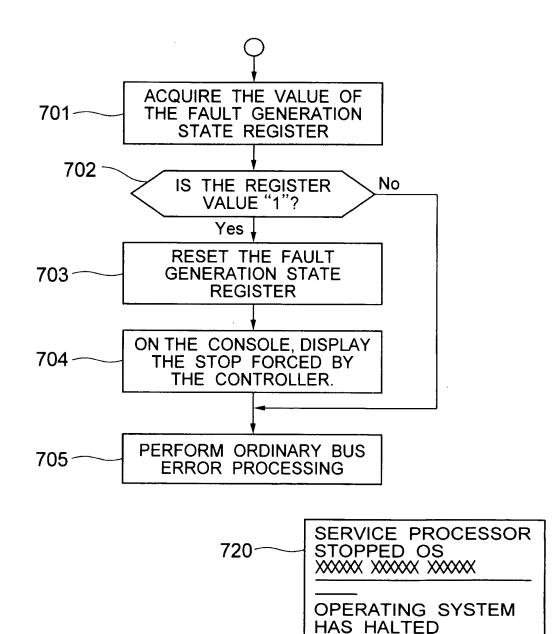


FIG. 7



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FIG. 8

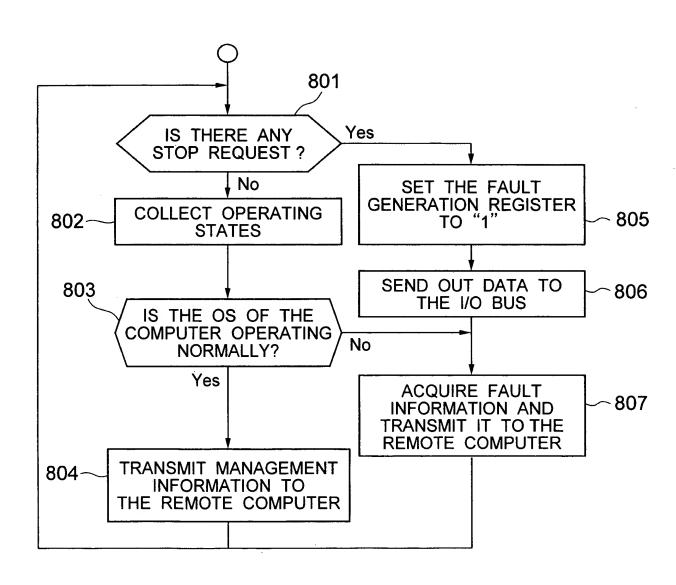
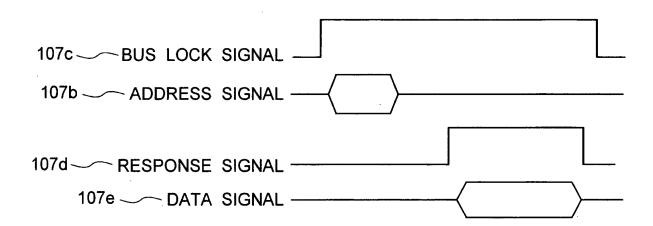
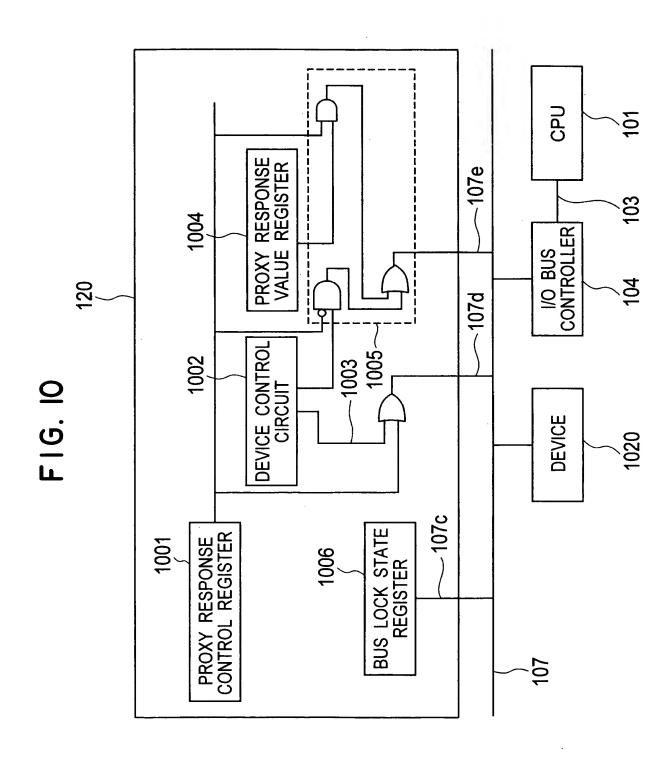


FIG. 9

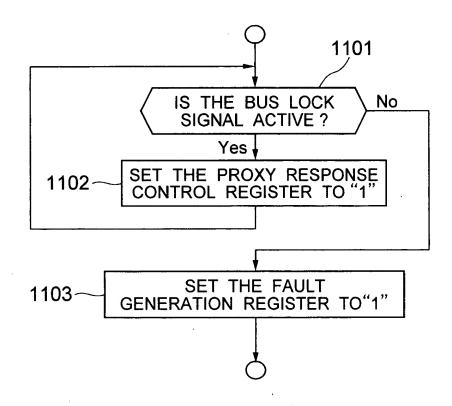


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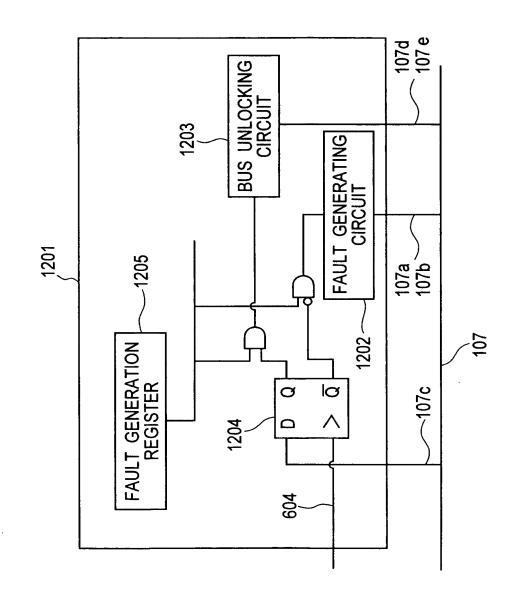


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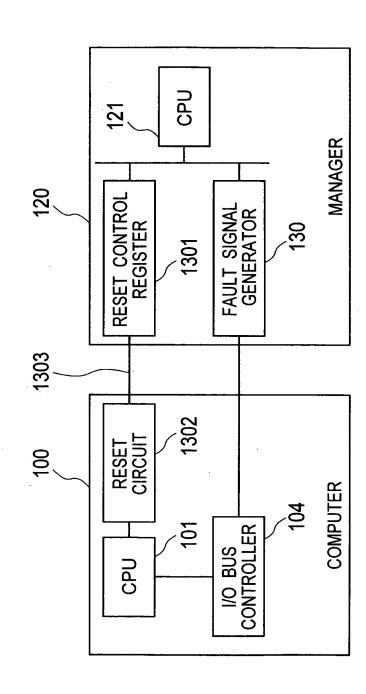


FIG. 14

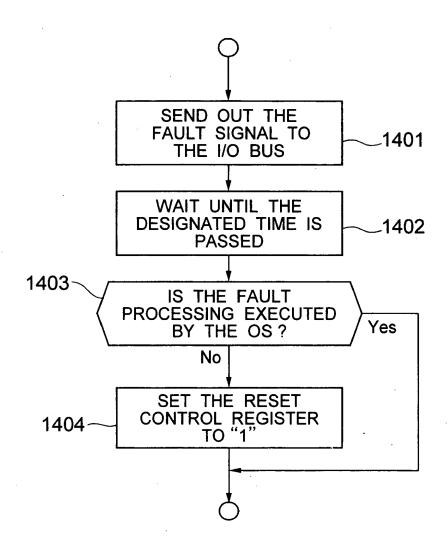


FIG. 15

